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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/811,162	03/26/2004	Francisc Sandulescu	NEWT . P0107US	1975
23908	7590 06/07/2006	EXAMINER		INER
RENNER OTTO BOISSELLE & SKLAR, LLP			UNELUS, ERNEST	
1621 EUCLID AVENUE NINETEENTH FLOOR			ART UNIT	PAPER NUMBER
CLEVELAND, OH 44115			2187	
			DATE MAILED: 06/07/2000	5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	
Office Action Summary		10/811,162	SANDULESCU ET AL.	
		Examiner	Art Unit	
		Ernest Unelus	2187	
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the c	orrespondence address	
A SH WHIC - Exter after - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATE of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. Period for reply is specified above, the maximum statutory period or reto reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be timwill apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).	
Status				
2a)☐	Responsive to communication(s) filed on <u>26 M</u> This action is FINAL . 2b) This Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro		
Dispositi	on of Claims			
5)□ 6)⊠ 7)□	Claim(s) 1-22 is/are pending in the application 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) 1-22 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	wn from consideration.		
Applicati	on Papers			
10)⊠	The specification is objected to by the Examine The drawing(s) filed on 26 March 2004 is/are: Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Example 2015.	a) ☐ accepted or b) ☑ objected to drawing(s) be held in abeyance. Sec tion is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).	
Priority (ınder 35 U.S.C. § 119			
12) a)	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bureau See the attached detailed Office action for a list	s have been received. s have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	ion No ed in this National Stage	
2) Notice	t(s) te of References Cited (PTO-892) te of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) tr No(s)/Mail Date 06/04/04; 03/25/05; 04/14/05	4) Interview Summary Paper No(s)/Mail Di 5) Notice of Informal P		

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DETAILED ACTION

1. The instant application having Application No. 10/811,162 has a total of 22 claims pending in the application; there are 3 independent claims and 19 dependent claims, all of which are ready for examination by the examiner.

I. INFORMATION CONCERNING OATH/DECLARATION

Oath/Declaration

2. The applicant's oath/declaration has been reviewed by the examiner and is found to conform to the requirements prescribed in **37 C.F.R. 1.63.**

II. INFORMATION CONCERNING DRAWINGS

Drawings

3. The applicant's drawings submitted are not acceptable for examination purposes.

III. ACKNOWLEDGEMENT OF REFERENCES CITED BY APPLICANT

4. As required by M.P.E.P. 609(C), the applicant's submissions of the Information Disclosure Statements dated June 04, 2004, March 25, 2005, April 14, 2005 and November 07, 2005 are acknowledged by the examiner and the cited references have been considered in the examination of the claims now pending. As required by M.P.E.P 609 C(2), a copy of the PTOL-1449 initialed and dated by the examiner is attached to the instant office action.

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IV. OBJECTIONS TO THE DRAWINGS

5. The subject matter of this application admits of illustration by a drawing to facilitate understanding of the invention. Applicant is required to furnish a drawing under 37 CFR 1.81(c). No new matter may be introduced in the required drawing. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d).

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the host video and the video interface must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an

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application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

V. REJECTIONS BASED ON PRIOR ART

Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. <u>Claims 1-22</u> are rejected under 35 U.S.C. 103(a) as being unpatentable over Ahern et al. (EP 1 075 111) in view of King et al. (US 2003/0131127).
- 8. As per claims 1 and 10, Ahern discloses a KVM and peripheral device switch (see paragraph 0006 and 007) comprising: a plurality of sets of KVM interfaces ("user interface modules" in fig. 1), each set of KVM interfaces having a keyboard interface, a mouse interface, and a video interface (see paragraph 006); a plurality of sets of host interfaces ("computer interface modules" in fig. 1), each set of host interfaces having a host keyboard and mouse interface and a host video interface (see paragraph 006); a master controller configured to switch at least

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one of the sets of KVM interfaces and at least one of the USB peripheral interfaces among the host interfaces;" (Page 5 of Applicant's specification identifies the master controller as "one or combinations of the following: programmable circuit, integrated circuit, memory and i/o circuits, an application specific integrated circuit, microcontroller, complex programmable logic device, field programmable gate arrays, other programmable circuits, or the like". With respect to this limitation, see paragraph 0007. The KVM and peripheral device switch is considered to comprise only one switch module (41) to which the user interfaces are connected, therefore this switch module is the master controller, see figures 1 and 2); wherein a keyboard and mouse host is emulated to the keyboard interface and the mouse interface (see paragraphs 0030 and 0034); and wherein a keyboard and a mouse is emulated to the host interface (see paragraph 0030). Ahern fail to disclose the KVM and peripheral switch device comprises also at least one USB peripheral interface, and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces

King discloses the KVM and peripheral switch device comprises also at least one USB peripheral interface, and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces (see fig. 2 of King and paragraph 0023).

Ahern et al. (EP 1 075 111) and King et al. (US 2003/0131127) are analogous art because they are from the same field of endeavor of how to increase the connectivity of the KVM switch.

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At the time of the invention it would have been obvious to a person of ordinary skill in the art to modify the KVM switch as taught by Ahern and to include also at least one USB peripheral interface in the KVM switch and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces as taught by King.

The motivation for doing so would have been because King teaches that ["The OCC keyboard 32 and mouse 34 devices may be PS/2 devices or USB devices or a combination of both. For example, it is possible to simultaneously attach both a PS/2 keyboard and a USB mouse. Because the USB protocol allows multiple devices to be attached to a single USB port by using a commercially available device known as a USB hub, multiple PS/2 devices and USB devices may be attached to a KVM unit 12" (paragraph 0023)].

Therefore, it would have been obvious to combine King et al. (US 2003/0131127) with Ahern et al. (EP 1 075 111) for the benefit of creating a KVM switch to obtain the invention as specified in claim 1.

9. As per <u>claim 2</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 1 (see rejection to claim 1 above): "at least one user controller communicably coupled to the master controller and at least one of the keyboard and mouse interfaces, the user controller being configured to emulate a keyboard and mouse host; and at least one computer controller communicably coupled to the master controller and at least one of the sets of host keyboard and mouse

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interfaces, the computer controller being configured to emulate a keyboard and a mouse" [(with respect to this limitation, Ahern discloses the user interface module as the user controller communicably coupled to the master controller (41) and at least one of the keyboard and mouse interfaces ("user interface modules" in fig.

1). Ahern also discloses where the user controller being configured to emulate a keyboard and mouse host (see paragraph 0030). (Ahern discloses the computer interface module (51) as the computer controller communicably coupled to the master controller (41) and at least one of the sets of host keyboard and mouse interfaces ("computer interface modules" in fig. 1). Ahern also discloses where the computer controller being configured to emulate a keyboard and a mouse (see paragraph 0030)]

10. As per <u>claim 3</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 2 (see rejection to claim 2 above): "wherein the at least one user controller and the at least one computer controller are the same controller" [(with respect to this limitation, Page 7 and 8 of Applicant's specification implied the at least one user controller and the at least one computer controller to be the same controller if they are control by a master controller. With respect to this understanding, Ahern's master controller clearly meets this limitation (see paragraph 0007)].

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- 11. As per <u>claim 4</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 2 (see rejection to claim 2 above): "wherein the at least one user controller and the at least one computer controller are communicably coupled" [with respect to this limitation, Ahern discloses the user interface module as the user controller and the computer interface module as the computer controller, which are communicably coupled (see fig. 1)].
- 12. As per <u>claim 5</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 2 (see rejection to claim 2 above): "wherein the master controller is configured to select which of the at least one user controllers and which of the at least one computer controllers will communicate with each other[with respect to this limitation, Ahern discloses emulation, which is perform by the master controller (switch module 41), between the at least one user controllers (user interface module) and with the at least one computer controllers (computer interface module) will communicate with each other (see paragraph 0030)].
- 13. As per <u>claims 6 and 17</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 5 (see rejection to claim 5 above): "wherein the master controller is configured to direct the selected user controller and the selected computer controller to communicate with each other" [with respect to this limitation, Ahern discloses emulation, which is perform by the master controller (switch module 41), between the selected user controllers (user interface module)

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with the selected computer controllers (computer interface module) to communicate with each other (see paragraph 0030)].

- 14. As per <u>claim 7</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 5 (see rejection to claim 5 above): "wherein the master controller is configured to select user controllers based on received user identification information and computer controllers based on computer identification information" [with respect to this limitation, (see paragraphs 0034 and 0047)].
- 15. As per <u>claim 8</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 1 (see rejection to claim 1 above): "further comprising a video switch communicably coupled to at least one of the video interfaces and at least one of the host video interfaces and configured to switch the video interfaces between the host video interfaces" [with respect to this limitation, (see paragraphs 0029 and 0030)]
- 16. As per <u>claim 9</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 8 (see rejection to claim 8 above): "wherein the video switch is communicably coupled to the master controller" [with respect to this limitation, (see fig. 1 and paragraph 0029)].
- 17. As per <u>claim 11</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 10 (see rejection to claim 10 above): "wherein the

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switch is a crosspoint matrix switch" [with respect to this limitation, (see fig. 1, which is a crosspoint matrix switch, as known in the art)]

- 18. As per claims 12 and 18, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 1 (see rejection to claim 1 above): "wherein the KVM and peripheral switch is compatible with both USB 1.x and USB 2.x." [with respect to this limitation, King discloses, in paragraph 0023, "Because the USB protocol allows multiple devices to be attached to a single USB port by using a commercially available device known as a USB hub". In other word, With the USB hub, someone is able to use a USB 1.x or a USB 2.x. for the motivation discloses above in claim 1]
- 19. As per <u>claims 13 and 19</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 1 (see rejection to claim 1 above): "wherein the KVM and peripheral device switch is capable of concurrently and independently switching keyboard and mouse interfaces between keyboard and mouse host interfaces and peripheral interfaces between host peripheral interfaces [with respect to this limitation, in paragraphs 0022 and 0045, Ahern discloses a concurrently and an independently step of switching keyboard and mouse interfaces between keyboard and mouse host interfaces and peripheral interfaces between host peripheral interfaces]

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- 20. As per <u>claims 14 and 20</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 1 (see rejection to claim 1 above): "wherein the keyboard interface and mouse interface are each selected from the group consisting of: SUN, PS/2, MAC, USB, Universal, and combinations thereof" [with respect to this limitation, King discloses a PS/2, (see paragraph 0023)]
- 21. As per <u>claim 15</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 1 (see rejection to claim 1 above): "further comprising a user interface selected from the group consisting of: buttons, RS232 commands, Ethernet, remote toggle switch, on-screen display, and combinations thereof" [with respect to this limitation, King discloses a PS/2, (see paragraph 0023)].
- 22. As per <u>claim 16</u>, Ahern discloses a KVM and peripheral device switch (see paragraph 0006 and 007) comprising: a plurality of sets of KVM interfaces ("user interface modules" in fig. 1), each set of KVM interfaces having a keyboard interface, a mouse interface, and a video interface (see paragraph 006); at least one user controller (user interface module in fig. 1) communicably coupled to at least one of the sets of KVM interfaces ("user interface modules" in fig. 1), the user controller being configured to emulate a keyboard and mouse host (see paragraph 0030); a plurality of sets of host interfaces ("computer interface modules" in fig. 1), each set of host interfaces having a host keyboard and mouse interface and a host

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video interface (see paragraph 006); at least one computer controller (computer interface module in fig. 1) communicably coupled to at least one of the sets of host interfaces ("computer interface modules" in fig. 1) (see fig. 1), the computer controller being configured to emulate a keyboard and a mouse (see paragraph 0030); and a peripheral switch (switching hub 40) communicably coupled to at least one of the peripheral interfaces ("user interface modules" in fig. 1) and to at least one of the host peripheral interfaces ("computer interface modules" in fig. 1; and configured to switch the peripheral interfaces between the host peripheral interfaces (see paragraph 0030); and a video switch communicably coupled to at least one video interface and to at least one host video interface and configured to switch the video interfaces between the host video interfaces (see paragraph 0029 and 0030); a master controller communicably coupled to the user controller, the computer controller (computer interface module in fig. 1), the peripheral switch (switching hub 40), and the video switch and configured to switch at least one of the sets of KVM interfaces and at least one of the peripheral interfaces between the host peripheral interfaces:" (Page 5 of Applicant's specification identifies the master controller as "one or combinations of the following: programmable circuit, integrated circuit, memory and i/o circuits, an application specific integrated circuit, microcontroller, complex programmable logic device, field programmable gate arrays, other programmable circuits, or the like". With respect to this limitation, see paragraph 0007. The KVM and peripheral device switch is considered to comprise only one switch module (41) to which the user interfaces are connected, therefore this switch module is the master

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controller, see figures 1 and 2 and paragraph 0030); Ahern fail to disclose the KVM and peripheral switch device comprises also at least one USB peripheral interface, and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces

King discloses the KVM and peripheral switch device comprises also at least one USB peripheral interface, and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces (see fig. 2 of King and paragraph 0023).

Ahern et al. (EP 1 075 111) and King et al. (US 2003/0131127) are analogous art because they are from the same field of endeavor of how to increase the connectivity of the KVM switch.

At the time of the invention it would have been obvious to a person of ordinary skill in the art to modify the KVM switch as taught by Ahern and to include also at least one USB peripheral interface in the KVM switch and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces as taught by King.

The motivation for doing so would have been because King teaches that ["The OCC keyboard 32 and mouse 34 devices may be PS/2 devices or USB devices or a combination of both. For example, it is possible to simultaneously attach both a PS/2 keyboard and a USB mouse. Because the USB protocol allows multiple devices to be attached to a single USB port by using a commercially available device known as a USB hub, multiple PS/2 devices and USB devices may be

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attached to a KVM unit 12" (paragraph 0023)].

Therefore, it would have been obvious to combine King et al. (US 2003/0131127) with Ahern et al. (EP 1 075 111) for the benefit of creating a KVM switch to obtain the invention as specified in claim 16

23. As per claim 21, Ahern discloses a KVM and peripheral device switch (see paragraph 0006 and 007) comprising: a plurality of sets of KVM interfaces ("user interface modules" in fig. 1), each set of KVM interfaces having a keyboard interface, a mouse interface, and a video interface (see paragraph 006); a plurality of sets of host interfaces ("computer interface modules" in fig. 1); emulating a keyboard and a mouse to each host interface (see paragraph 0030); emulating a host to each keyboard interface and mouse interface (see paragraph 0030); receiving a switching command at a controller (the switch module) (see paragraph 0030), the switching command containing identification information (see paragraph 0047); and using the identification information to connect at least one of the keyboard interfaces, at least one of the mouse interfaces, at least one of the video interfaces, and at least one of the peripheral interfaces to at least one of the host interfaces (see paragraphs 0034 and 47). Ahern fail to disclose the KVM and peripheral switch device comprises also at least one USB peripheral interface, and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces

King discloses the KVM and peripheral switch device comprises also at least one USB peripheral interface, and the master controller is configured to switch also at least

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one of the USB peripheral among the host interfaces (see fig. 2 of King and paragraph 0023).

Ahern et al. (EP 1 075 111) and King et al. (US 2003/0131127) are analogous art because they are from the same field of endeavor of how to increase the connectivity of the KVM switch.

At the time of the invention it would have been obvious to a person of ordinary skill in the art to modify the KVM switch as taught by Ahern and to include also at least one USB peripheral interface in the KVM switch and the master controller is configured to switch also at least one of the USB peripheral among the host interfaces as taught by King.

The motivation for doing so would have been because King teaches that ["The OCC keyboard 32 and mouse 34 devices may be PS/2 devices or USB devices or a combination of both. For example, it is possible to simultaneously attach both a PS/2 keyboard and a USB mouse. Because the USB protocol allows multiple devices to be attached to a single USB port by using a commercially available device known as a USB hub, multiple PS/2 devices and USB devices may be attached to a KVM unit 12" (paragraph 0023)].

Therefore, it would have been obvious to combine King et al. (US 2003/0131127) with Ahern et al. (EP 1 075 111) for the benefit of creating a KVM switch to obtain the invention as specified in claim 21

24. As per <u>claim 22</u>, the combination of Ahern and King disclose the KVM and peripheral device switch of claim 21 (see rejection to claim 21 above): "further

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comprising: (a) determining whether the peripheral interface is to be switched concurrently with the keyboard interface and the mouse interface (); (b) concurrently switching the peripheral interface with the keyboard interface and mouse interface upon a positive determination in step (a) [with respect to this limitation, in paragraph 0045, Ahern discloses a concurrent step of switching keyboard and mouse interfaces between keyboard and mouse host interfaces and peripheral interfaces between host peripheral interfaces, which is done by a logic device. It is well known in the art that a control logic device function base upon a determination, which in this case would be a positive determination].

VI. RELEVANT ART CITED BY THE EXAMINER

- 25. The following prior art made of record and not relied upon is cited to establish the level of skill in the applicant's art and those arts considered reasonably pertinent to applicant's disclosure. See MPEP 707.05(c).
- 30. The following reference teaches of how to increase the connectivity of the KVM switch.

U.S. PATENT NUMBER

US 5,721,842

US 6,324,605

US 5,721,842

VII. CLOSING COMMENTS

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Conclusion

a. STATUS OF CLAIMS IN THE APPLICATION

26. The following is a summary of the treatment and status of all claims in the application as recommended by M.P.E.P. 707.07(i):

a(1) CLAIMS REJECTED IN THE APPLICATION

27. Per the instant office action, claims 1-22 have received a first action on the merits and are subject of a first action non-final.

b. DIRECTION OF FUTURE CORRESPONDENCES

28. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ernest Unelus whose telephone number is (571) 272-8596. The examiner can normally be reached on Monday to Friday 9:00 AM to 5:00 PM.

IMPORTANT NOTE

29. If attempts to reach the above noted Examiner by telephone are unsuccessful, the Examiner's supervisor, Mr. Donald Sparks, can be reached at the following telephone number: Area Code (571) 272-4201.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through

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Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

May 18, 2006

Ernest Unelus

Examiner

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DONALD SPARKS